

Appl. No. 10/064,215  
Amdt. dated November 15, 2005  
Reply to Office action of August 23, 2005

**Amendments to the Specification:**

Please replace the title of the invention with the following:

**Clock and Data Recovery Circuit and Related Methods**

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Please replace the Abstract of the disclosure with the following:

**CLOCK AND DATA RECOVERY CIRCUIT AND RELATED METHODS**

10 The present invention provides a data A clock recovery circuit for generating an output signal that is synchronized with an input signal. The data clock recovery circuit includes a charge pump, a first filter, an oscillator, a switch circuit, and a second filter. When the charge pump operates, the switch circuit will disconnect the first filter from the oscillator. Additionally, when the charge pump stops operating, the switch circuit will connect the 15 first filter and the oscillator such that the oscillator adjusts a frequency or phase of the output signal according to the output voltage of the first filter.